

FIG. 1

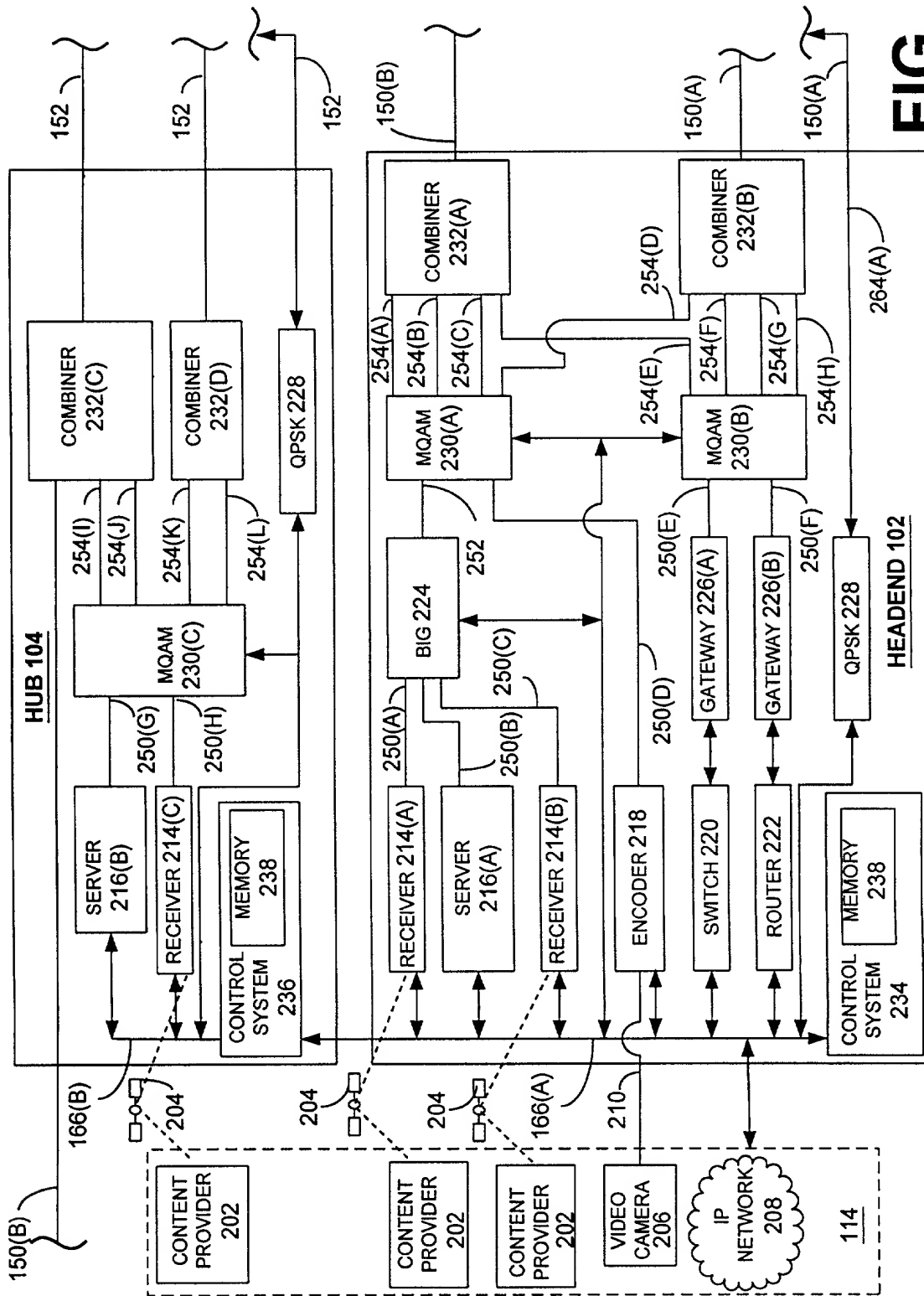


FIG. 2

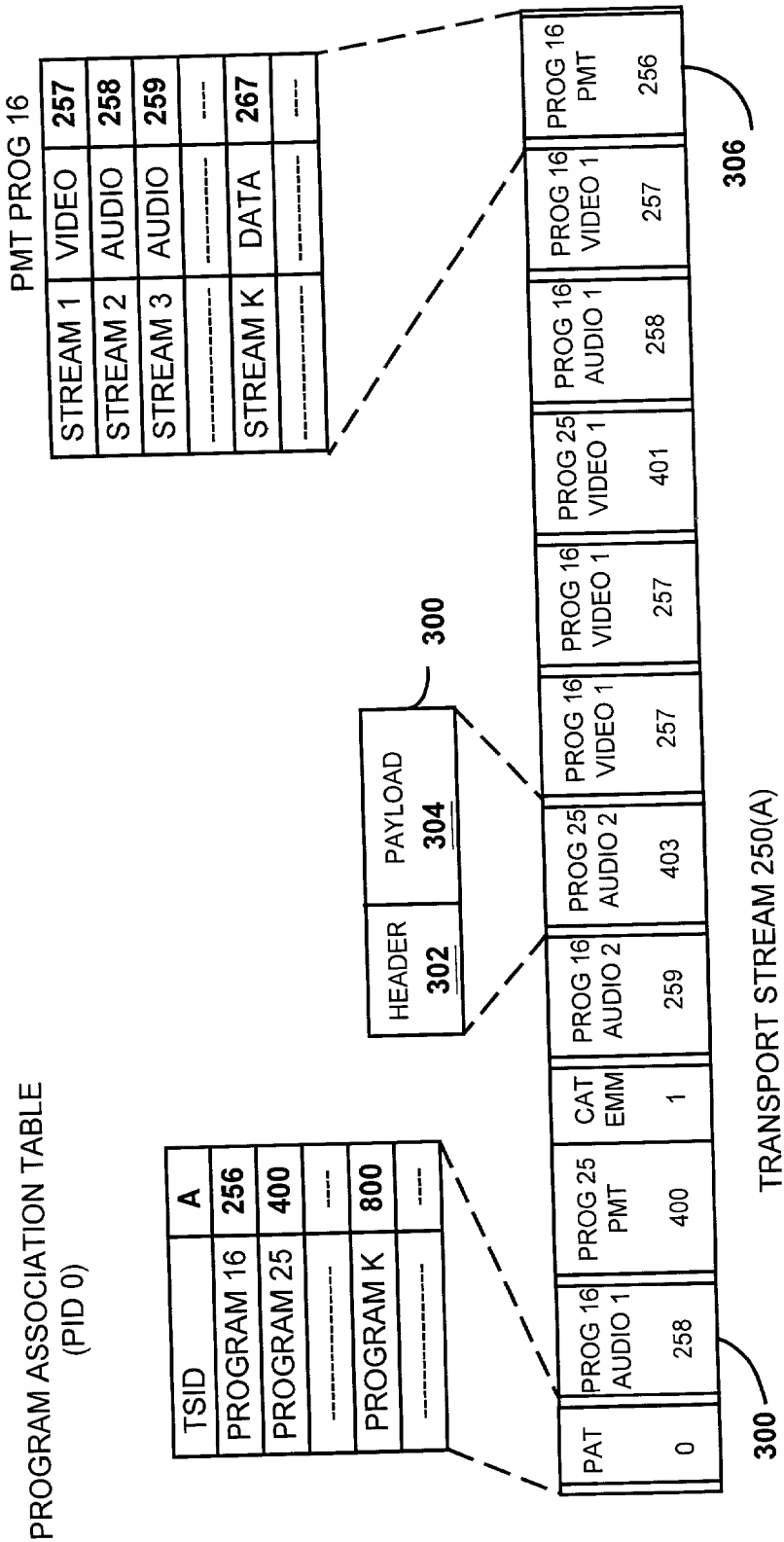
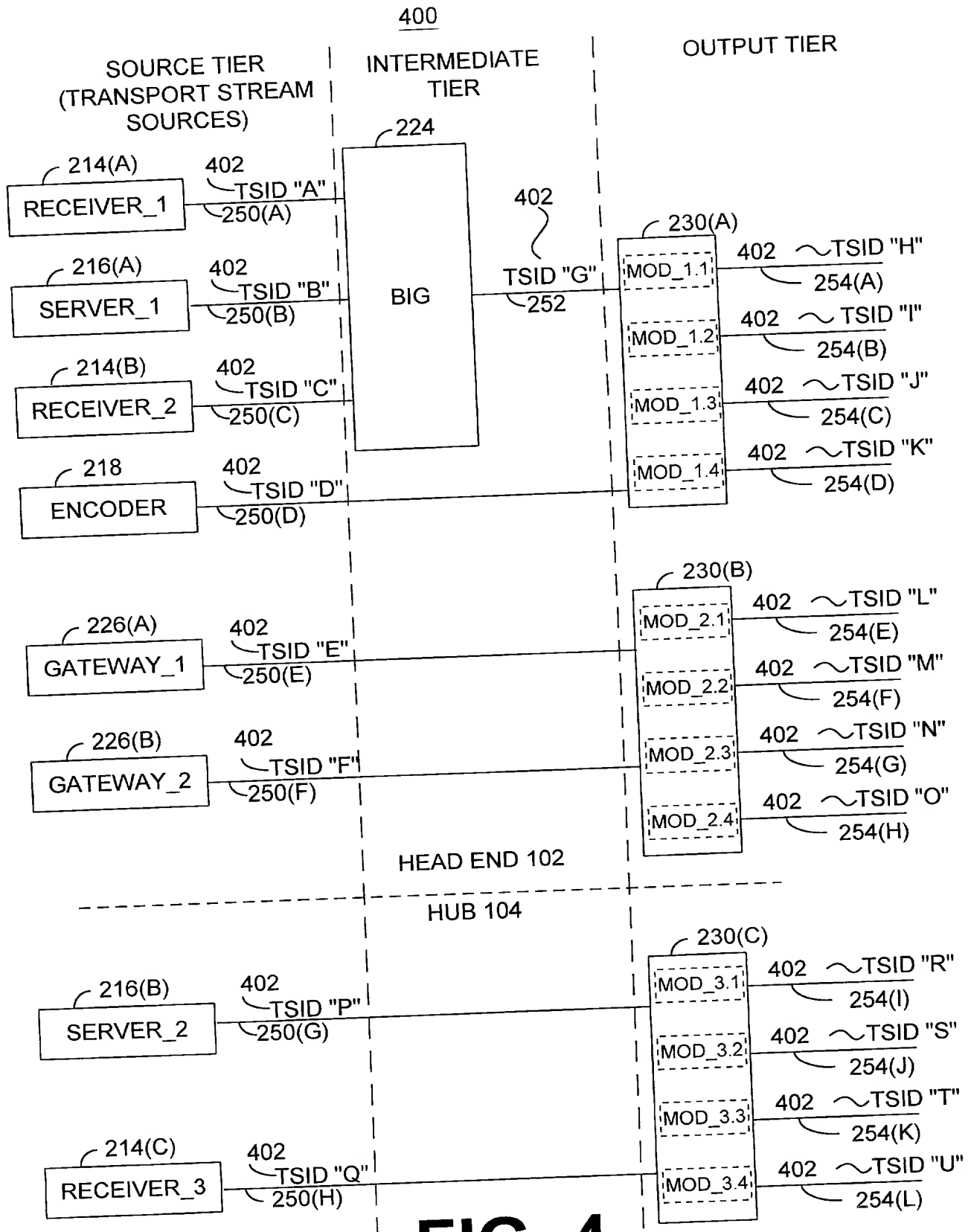


FIG. 3

**FIG. 4**

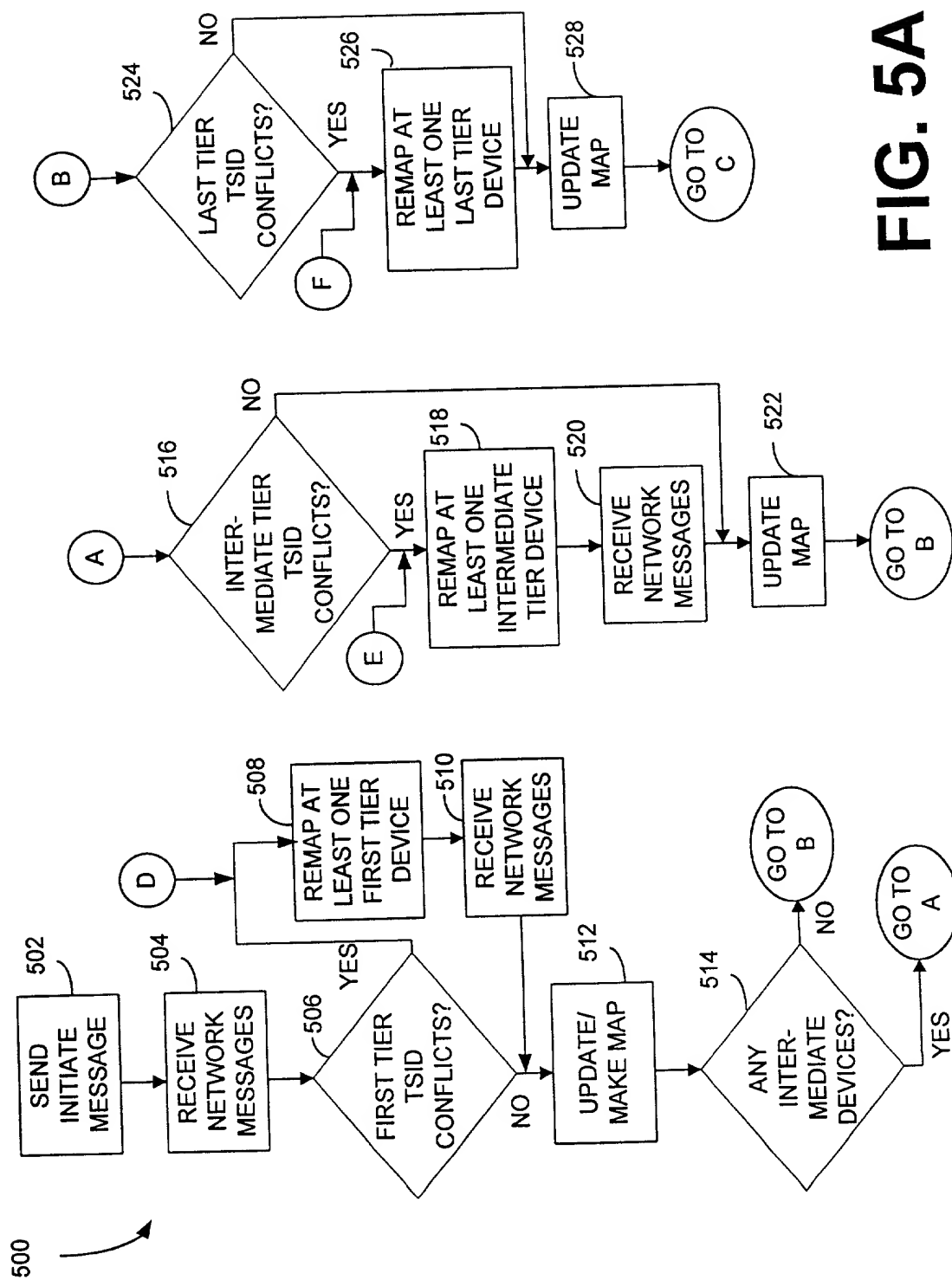
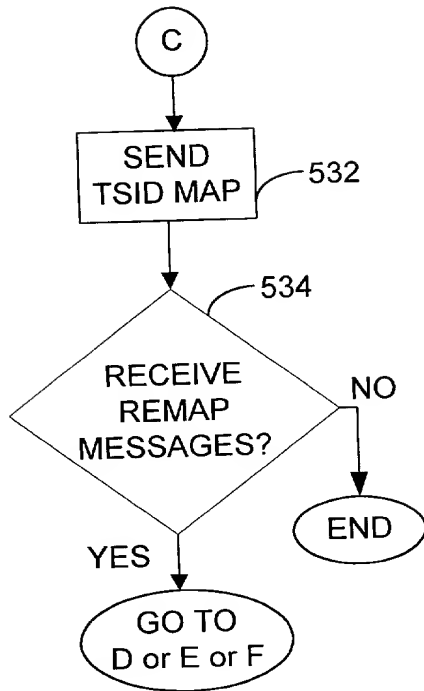
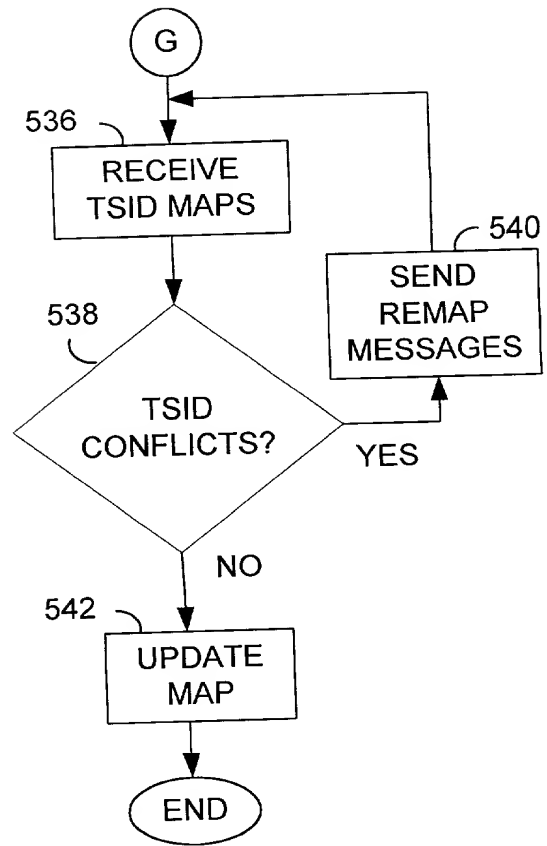
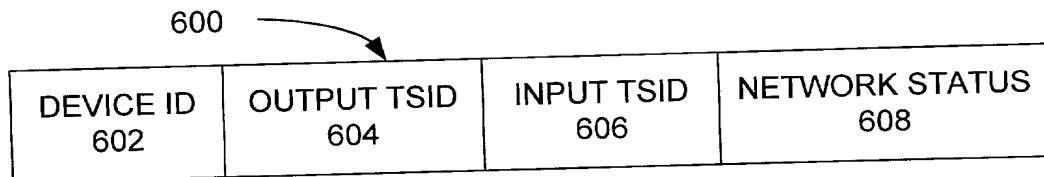


FIG. 5A

**FIG. 5B****FIG. 5C****FIG. 6**

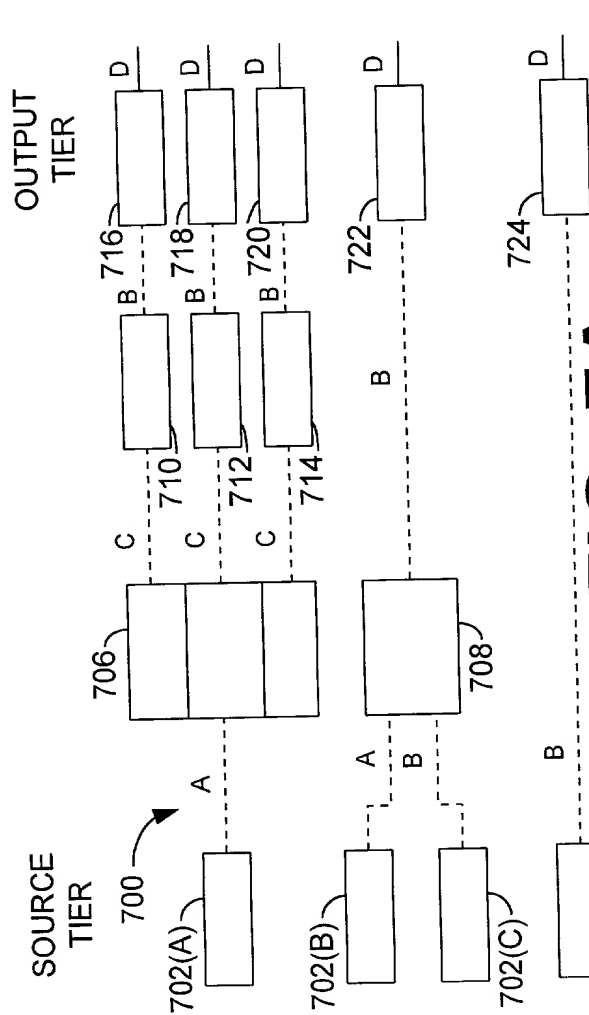


FIG. 7A

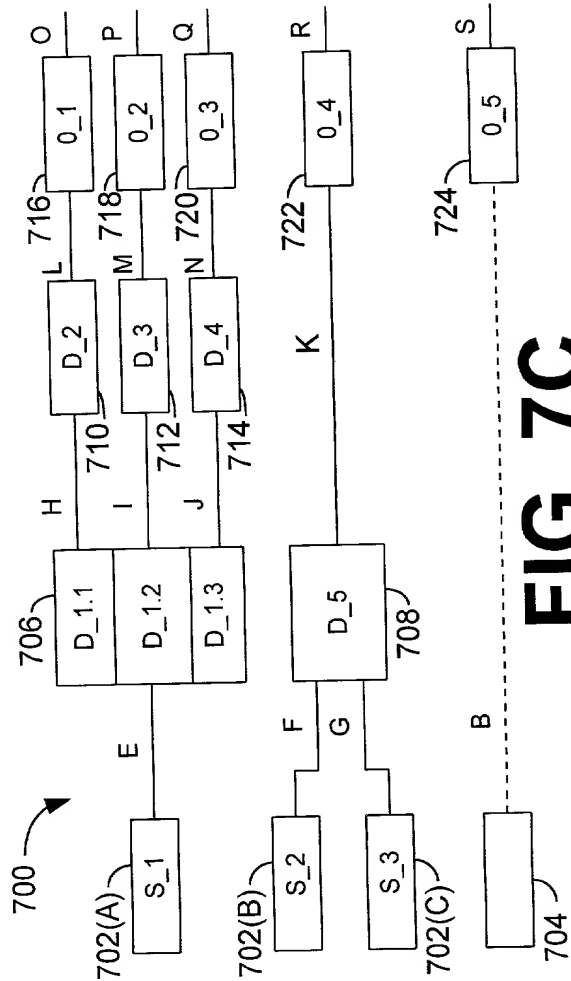


FIG. 7C

752	754(A)		754(B)		756(B)	
	DEVICE TYPE	INPUT TSID	OUTPUT TSID	INPUT TSID	OUTPUT TSID	OUTPUT TSID
	S_1 (702A)	---	A	---	E	E
	S_2 (702B)	---	A	---	F	F
	S_3 (702C)	---	B	---	G	G
	D_1.1 (706)	A	C	E	H	H
	D_1.2 (706)	A	C	E	I	I
	D_1.3 (706)	A	C	E	J	J
	D_5 (708)	(A/B)	B	(F/G)	K	K
	D_2 (710)	C	B	H	L	L
	D_3 (712)	C	B	I	M	M
	D_4 (714)	C	B	J	N	N
	O_1 (716)	B	D	L	O	O
	O_2 (718)	B	D	M	P	P
	O_3 (720)	B	D	N	Q	Q
	O_4 (722)	B	D	K	R	R
	O_5 (724)	B	D	B	S	S

FIG. 7B

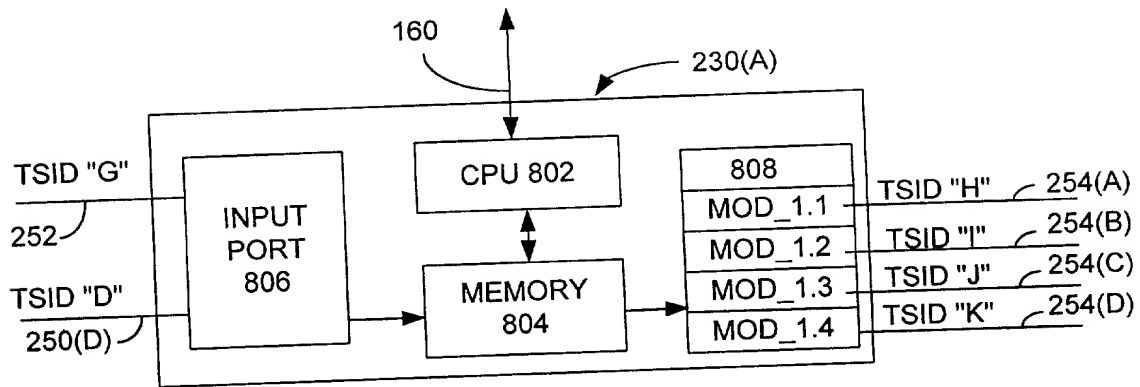


FIG. 8

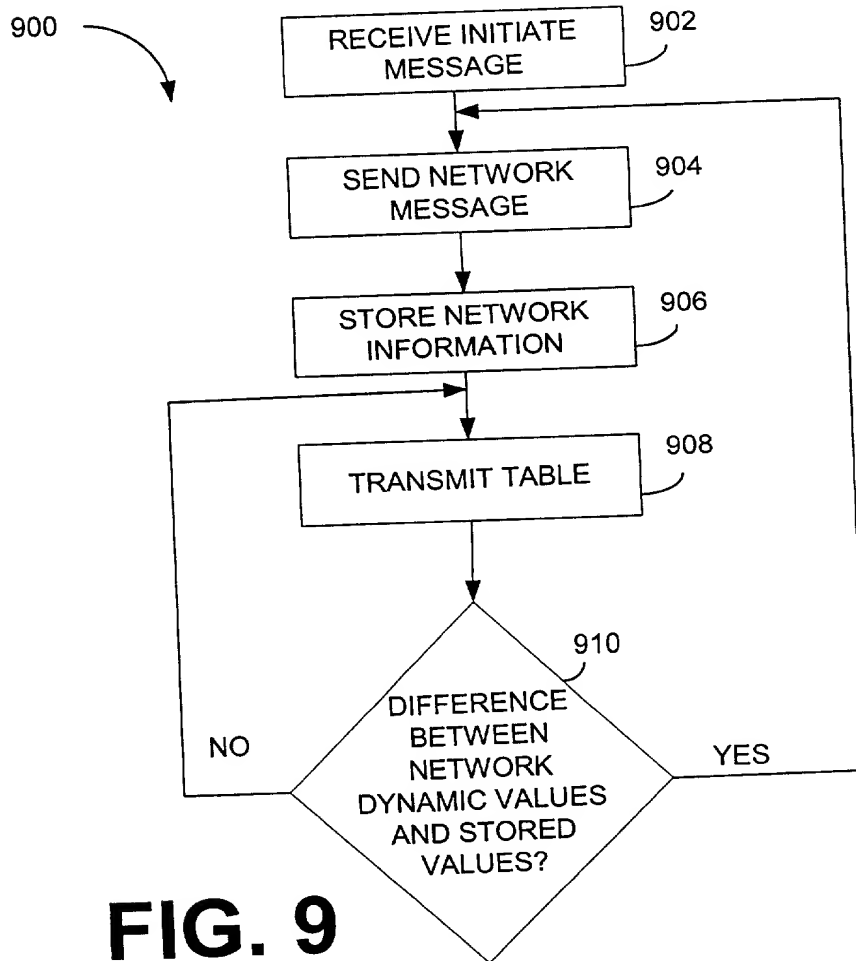
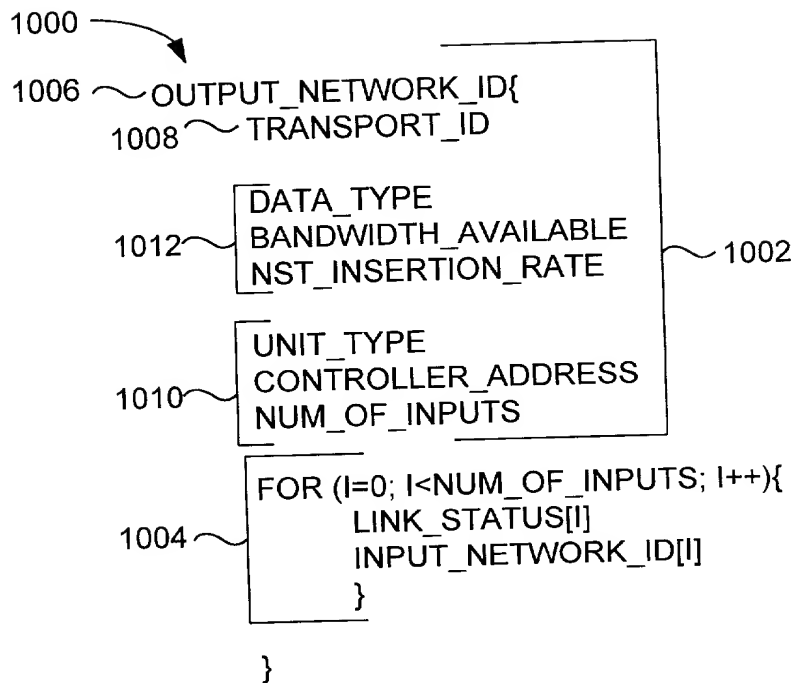


FIG. 9

**FIG. 10**

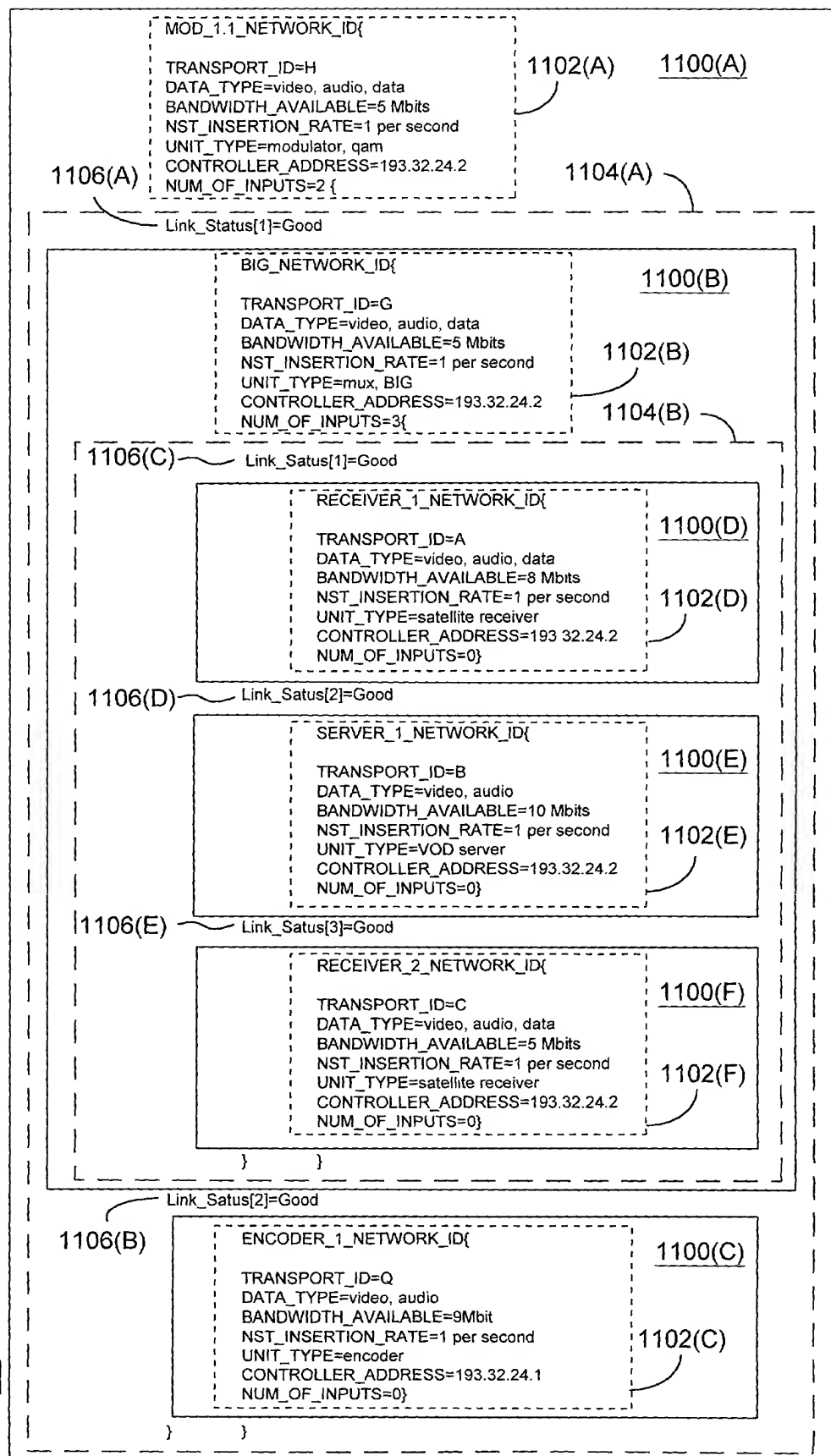


FIG. 11